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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Takatoshi DEGUCHI et al.**

Group Art Unit: **2813**

Serial No.: **10/695,666**

Examiner: **Nema O. Berezny**

Filed: **October 29, 2003**

Confirmation No.: **4071**

For: **METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE
HAVING A MULTILAYER INTERCONNECTION STRUCTURE
(As Amended Herein)**

Attorney Docket Number: **032071**

Customer Number: **38834**

AMENDMENT UNDER 37 C.F.R. §1.111

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Date: February 17, 2005

Sir:

This paper is filed in response to the Office Action dated December 10, 2004.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims that begins on page 3 of
this paper.

Remarks/Arguments begin on page 10 of this paper.